



Express Mail No. EV 346 812 511 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Stephen Maxwell PARKES

Confirmation No. 4765

Application No.: 10/679,978

Group Art Unit: 2819

Filed: October 6, 2003

Examiner: Daniel D. Chang

For: INTEGRATED CIRCUIT AND
RELATED IMPROVEMENTS

Attorney Docket No.: 85170-4800

**THIRD SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Pursuant to applicant's duty of disclosure under 37 C.F.R. 1.56, enclosed is a PTO form 1449 which lists one (1) reference for the Examiner's review and consideration. A copy of this reference was enclosed with the Information Disclosure Statement filed on January 26, 2004, and another copy is enclosed herewith for the Examiner's convenience marked to include publication details including the date of publication. It is respectfully requested that the reference be made of record in this application by the Examiner's completion and return of the PTO Form 1449.

No fee is believed to be due for this submission, as the reference has already been filed in the application. However, if a fee is due, it is believed to be \$180.

Please charge any required fees to Winston & Strawn LLP Deposit Account
No. 50-1814.

Respectfully submitted,

Date: January 27, 2005

for Stephen Filippini (Reg. No. 33,384)
Allan A. Fanucci (Reg. No. 30,256)
WINSTON & STRAWN LLP
CUSTOMER NO. 28765
(212) 294-3311

Enclosures

ATTY. DOCKET NO.:

85170-4800

APPLICATION SERIAL NO.:

10/679,978

APPLICANT:

Stephen Maxwell PARKES

FILING DATE:

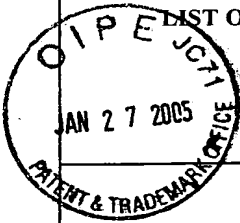
October 6, 2003

GROUP:

2819

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)



U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

	AA	Guerrier et al. "A Generic Architecture for On-Chip Packet-Switched Interconnections", Proceedings of the Design Automation and Test in Europe, pages 250-256, Paris, France, March 2000.
--	----	---

EXAMINER	DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.